

<b>Notice of References Cited</b>	Application/Control No. 09/975,256	Applicant(s)/Patent Under Reexamination RAMKUMAR ET AL.
	Examiner David L. Hogans	Art Unit 2813

**U.S. PATENT DOCUMENTS**

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C	US-5,464,792	11-1995	Tseng et al.	438/585
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L	US-			
M	US-			

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**NON-PATENT DOCUMENTS**

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)		
U	Wolf et al., Silicon Processing for the VLSI Era (2000), Lattice Press, Second Edition, Volume 1, page 198.		
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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